

## WHAT IS CLAIMED IS

1        1.    A liquid crystal display control device for receiving an input  
2        video signal and outputting display data corresponding to the  
3        video signal to a liquid crystal display panel to display the  
4        picture of the display data on the liquid crystal display panel,  
5        comprising:

6                a storage element for storing the input video signal; and  
7                memory control means for controlling said storage element  
8        to store the input video signal at the timing corresponding to the  
9        input timing of the video signal and to read out the video signal  
10       from said storage element at the timing corresponding to the  
11       output timing of the display data to said liquid crystal display  
12       panel.

1        2.    A liquid crystal display control device for receiving an  
2        input video signal and displaying the picture corresponding to the  
3        video signal on a liquid crystal display, comprising:

4                a frame memory for storing the input video signal;  
5                a line memory for storing a video signal read out from said  
6        frame memory;

7                memory control means for controlling the data write-in and  
8        read-out operation of the video signal in and from said frame  
9        memory and said line memory; and

10               a calculation processing circuit for performing  
11       predetermined processing on the video signal read out from said  
12       frame memory and the video signal read out from said line

memory, and then outputting the processed video signals to said liquid crystal display panel, wherein said memory control means synchronizes the read-out of the video signal from said frame memory and the write-in of the video signal into said frame memory every predetermined time interval.

3. The liquid crystal display control device as claimed in claim 2, wherein said frame memory has a storage capacity corresponding to two lines of the input video signal.

4. A liquid crystal display control device for receiving an input video signal and displaying a picture corresponding to the video signal on a liquid crystal display panel, comprising:

a frame memory for storing the input video signal;

a memory mount portion for mounting thereon a line memory which is separately provided to store a video signal read out from said frame memory;

memory control means for controlling the input/output operation of the video signal to/from said frame memory and the input/output operation of the video signal to/from said line memory mounted on said memory mount portion; and

a calculation processing circuit for performing predetermined processing on the video signal read out from said frame memory or the video signals read out from both said frame memory and said line memory mounted on said memory mount portion, and then outputting the processed signal to said liquid

17 crystal display panel.

1 5. The liquid crystal display control device as claimed in claim  
2 4, wherein said calculation circuit is designed to change its  
3 processing content in accordance with the presence or absence of  
4 said line memory.

1 6. The liquid crystal display control device as claimed in claim  
2 5, wherein said memory mount portion is designed so that a  
3 memory card can be mounted on said memory mount portion.

1 7. The liquid crystal display control device as claimed in claim  
2 2 wherein the processing which is performed by said calculation  
3 processing circuit includes the enlargement/reduction processing  
4 of the picture corresponding to the video signal.

1 8. A liquid crystal display control device for receiving an input  
2 video signal and displaying the picture corresponding to the video  
3 signal on the liquid crystal display panel, characterized by  
4 comprising:

5 resolution judgment means for judging the resolution of the  
6 input video signal;

7 first processing means for directly outputting the video  
8 signal as a bypass video signal;

9 second processing means for performing predetermined  
10 processing on the input video signal and then outputting the signal

11 as a processed signal; and

12 timing adjusting means for adjusting the output timing of  
13 the signal output from said first processing means or said second  
14 processing means to said liquid crystal display panel,

15 wherein said first processing means outputs the bypass  
16 video signal when the resolution of the video signal which is  
17 judged by said resolution judgment means is coincident with the  
18 resolution of said liquid crystal display panel, and stops the  
19 output of the bypass video signal when the resolution of the video  
20 signal which is judged by said resolution judgment means is not  
21 coincident with the resolution of said liquid crystal display  
22 panel, and

23 said second processing means stops the output of the  
24 processed signal when the resolution of the video signal which is  
25 judged by said resolution judgment means is coincident with the  
26 resolution of said liquid crystal display panel, and outputs the  
27 processed signal when the resolution of the video signal which is  
28 judged by said resolution judgment means is not coincident with  
29 the resolution of said liquid crystal display panel.

1 9. The liquid crystal display control device as claimed in claim  
2 8, wherein said second processing means performs the  
3 enlargement processing on the video signal.